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Patent 7,042,274

PATENT APPLICATIONIN UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No.: 7,042,274

Docket No: 80107.040US1

Issue Date: May 9, 2006

Patentee: Peter Hazucha et al.

Title REGULATED SLEEP TRANSISTOR APPARATUS, METHOD, AND SYSTEM

REQUEST FOR CERTIFICATION OF CORRECTION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

It is requested that a Certificate of Correction be issued correcting printing errors appearing in the above-identified United States patent. Two copies of the text of the Certificate in the suggested form are enclosed.

Pursuant to 1.20(a), the examiner is authorized to charge the Certificate of Correction fee of \$100.00 to the Credit Card (PTO-2038 Authorization Form enclosed).

Issuance of the Certificate of Correction would neither expand nor contract the scope of the claims as properly allowed, and re-examination is not required.

Respectfully submitted,

Customer Number: 45445

952-473-8800

Certificate
JUN 08 2006
of Correction

Date May 31, 2006
P.O. Box 52050
Minneapolis, MN 55402

By Dana B. LeMoine
Dana B. LeMoine
Reg. No.: 40,062

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Name Kirstin Ryan

Signature Kirstin Ryan

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Peter Hazucha et al.

Examiner: Vu, Bao

Patent No.: 7,042,274

Group Art Unit: 2838

Issue Date: May 9, 2006

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Title: REGULATED SLEEP TRANSISTOR APPARATUS, METHOD, AND SYSTEM

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

We are transmitting herewith the attached:

- ☒ Authorization to charge the Certificate of Correction fee of \$100.00 to the Credit Card.
- ☒ Request for Certificate of Correction.
- ☒ Certificate of Correction Form - PTO-1050 (in duplicate)
- ☒ PTO-2038 Credit Card Authorization Form (1 pg.)
- ☒ A return postcard.

Date May 31, 2006
Customer Number: 45445

By Dana B. LeMoine
Dana B. LeMoine
Reg. No.: 40,062

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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : 7,042,274

DATED : May 9, 2006

INVENTOR(S) : Hazucha et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the face page, in field (56), under "U.S. Patent Documents", in column 2, line 2, delete "6,384,674 B1" and insert - - 6,384,674 B2 - -, therefor.

On the face page, in field (56), under "U.S. Patent Documents", in column 2, line 3, above "6,744,301" insert - - 6,677,735 B2 1/2004 Xi 323/273 - -.

On the face page, in field (56), under "U.S. Patent Documents", in column 2, line 4, delete "6,765,414 B1" and insert - - 6,765,414 B2 - -, therefor.

On the face page, in field (56), under "U.S. Patent Documents", in column 2, line 4, below "6,765,414" insert - - 6,775,784 B1 8/2004 Park 713/320
6,806,690 B2 10/2004 Xi 323/273 - -.

In column 9, line 11, in Claim 14, after "integrated" insert - - circuit - -.

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